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10550326 - GAI: 2439

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	Filing Date		2005-09-23	
	First Named Inventor	Roger D. Chamberlain		
	Art Unit	2439		
	Examiner Name	Tolentino, Roderick		
	Attorney Docket Number	53047-57365		

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/R.T./	2	NIEWCZAS ET AL., "A Pattern Matching Algorithm for Verification and Analysis of Very Large IC Layouts", ACM, April 1998, pp. 129-134	<input type="checkbox"/>
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